

ABSTRACT:

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The invention relates to an amplifier circuit 50 comprising:

- a pre-amplifying stage 41,
- an amplifying stage 42, and
- identification means 45 for identifying the configuration of the amplifying stage 42.

5 In accordance with the invention, adaptation means 46 enable to configure the pre-amplifying stage 41 in such a manner that it supplies the amplifying stage 42 with:

- either two signals which are in phase opposition if the amplifying stage is configured so as to be in the symmetrical mode,
 - or two signals which are in phase if the amplifying stage is configured so as to be in the
- 10 asymmetrical mode.

The invention enables an amplifier to be configured in the symmetrical or asymmetrical mode, in a flexible manner, the performance of said amplifier being optimal irrespective of the configuration chosen.

15 Application: Amplification of tuner output signals.

Reference: Fig. 3